

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
INFORMATION DISCLOSURE STATEMENT

APPLICANT: Yuji Takaoka, et al. **ATTY DOCKET NO.:** 9792909-5767
SERIAL NO.: not yet assigned **GROUP ART UNIT:** not yet assigned
DATE FILED: November 21, 2003 **EXAMINER:** not yet assigned
INVENTION: "SEMICONDUCTOR DEVICE AND PROCESS FOR FABRICATING
THE SAME"

which is a Divisional of the following copending U.S. Application:

APPLICANT: Yuji Takaoka, et al. **ATTY DOCKET NO.:** 9792909-4794
SERIAL NO.: 09/816,055 **GROUP ART UNIT:** 2813
DATE FILED: March 23, 2001 **EXAMINER:** N. Berezny
INVENTION: "SEMICONDUCTOR DEVICE HAVING A PLURALITY OF
ENCRUSTED SEMICONDUCTOR CHIPS (As Amended)"

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

S I R:

In accordance with the provisions of 37 C.F.R. § 1.56, Applicants request that citation and examination of the references identified on the attached PTO-1449 form, copies of which are enclosed herewith in accordance with 37 C.F.R. §1.98, be made during the course of examination of the above-referenced application for United States Letters Patent.

I. SUBMITTED UNITED STATES PATENT REFERENCES

<u>Reference</u>	<u>Patent Number</u>	<u>Inventor</u>	<u>Issue Date</u>
AA	6,159,767	C. Eichelberger, et al.	December 12, 2000
AB	5,475,264	T. Sudo, et al.	December 12, 1995
AC	6,552,633	S. Sharma	September 3, 1996
AD	6,239,980	R. Fillion, et al.	May 29, 2001
AE	6,284,563	J. Fjelstad	September 4, 2001

II. SUBMITTED FOREIGN PATENT REFERENCES

<u>Reference</u>	<u>Patent Number</u>	<u>Country</u>	<u>Date of Publication</u>
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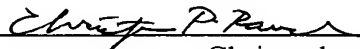
III. OTHER ITEMS OF INFORMATION

AT *"Modern Dictionary of Electronics"*, Rudolf F. Graf, Editor, 1997, p. 507

IV. EXPLANATION OF RELEVANCE

The above references were made of record in parent application U.S. Serial No. 09/816,055.

Submitted by,

 (Reg. 45,034)

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37 CFR 1.501 INFORMATION DISCLOSURE STATEMENT IN A PATENT (use several sheets if necessary)					Docket No. 09792909-5767		Serial No.	
					Applicants: Yuji Takaoka, et al.			
					Filing Date November 21, 2003		Group Art Unit	
U.S. PATENT DOCUMENTS								
Examiner's Initials		Document Number	Date	Name	Class	Subclass	Filing Date If appropriate	
	AA	6,159,767	12-12-00	C. Eichelberger				
	AB	5,475,264	12-12-95	T. Sudo				
	AC	5,552,633	9-3-96	S. Sharma				
	AD	6,239,980	5-29-01	R. Fillion				
	AE	6,284,563	9-4-01	J. Fjelstad				
	AF							
	AG							
FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AH							
	AI							
	AJ							
	AK							
	AL							
	AM							
	AN							
	AO							
	AP							
	AQ							
	AR							
	AS							
	AT							
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)								
	AU	<i>"Modern Dictionary of Electronics"</i> , Rudolf F. Graf, Editor, 1997, p. 507						
	AV							
	AW							
	AX							
	AY							
	AZ							
Examiner			Date Considered					
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								